TERMINAL PARITY SETTINGS

Many Data General terminal interfaces (multiplexors, etc.) are configured for 7 data bits, 1 stop bit, and NO parity. There is, however, no switch setting on the MIRAGE 1#2 Processor board to reflect this particular configuration.

This Application Note suggests settings which should be used in such an environment.

Set the CRT configuration switches to 7 data bits, 1 stop bit, and EVEN parity. Refer to the MIRAGE Reference Manual, pages 5-12 through 5-17 for details on setting these switches.

Why will EVEN parity on the MIRAGE Processor work with NO parity at the CRT? Because the MIRAGE will normally ignore parity errors from the keyboard, and the CRT's display will ignore parity errors.

If, instead, you want to enable parity checking by the MIRAGE Processor, use the DGCRT utility described on page 1-22 of the MIRAGE Reference Manual.

Remember, if you are using the MIRAGE pass-through facility, the MIRAGE CRT switch settings must be selected to match the characteristics of the host interface, except as described in this Application Note.

Make sure to set the switches!

When MIRAGE boards are shipped, the DIP switches are usually in the down position. This sets the board for 8 data bits, 1 stop bit and odd parity.

ALTHOUGH THIS MAY APPEAR TO WORK WITH TERMINALS SET FOR 7 DATA BITS, DON'T COUNT ON IT!

If your CRT is set for 7 data bits plus parity, and MIRAGE is set for 8 data bits, MIRAGE will see the parity bit as part of the data, and may not, therefore, be able to recognize certain characters properly. Non-functioning control characters may be noticed first, and, in particular, control-Z may not work with the CP/M editor, ED.

* This Application Note replaces note #04 dated 9 September 1983.