


1. TELETYPE OUTPUT

| 0 | 060477 | READS | Start © location $\varnothing$ |
| :--- | :--- | :--- | :--- |
| 1 | 061111 | DOAS, 0 | Set the lower 8 data |
| 2 | 063611 | SKPDN | Switches to the bits |
| 3 | 000777 | JMP. -1 | you desire to output. |
| 4 | 000000 | JMP 0 | Allows scoping output <br> circuit. |

2. TELETYPE IN/PUT

0060110 NIO S
1063610 SKPDN
2000777 JMP.-1
3074510 DIAS,3
4063610 SKPDN
5000777 JMP.-1

- ococol $\operatorname{jmp} z$

3. ECHO KEYBOARD

0063610 SKPDN
1000777 JMP.-1
2060610 DIAC, D
3063710 SKPDZ
4000777 JMP.-1
5061111 DOAS, 0
6000000 JMP O
4. ECHO TAPE \#1

0060110 NIO,S
1063610 SKPDN
2000777 JMP-1
3060610 DIAC, 0
4063510 SKPBZ
5000777 JMP-1
6061111 DOAS,D
7063611 SKPDN
10000777 JMP-1
11000000 JMPO

Start @ location 0 reads any paper tape, continually, into $A C^{3}$ Allows scoping input circuits.

Starts @ location Ø Character typed on keyboard should be printed on printer keyboard $\rightarrow$ CPU $\rightarrow$ Printer
5. ECHO TAPE \#2
$0060110 \quad$ Ditto \#4
1063670
2000777
3060510
4063511
5000777
6061111
7000001


Field Service


Subject: TROUBLESHOOTING AID FOR NOVA LINE MEMORIES:

This DIB will check for any drivers which would hold down the read and write source voltages on all Nova line Memories.

The 3 watt current resistors on the memories should not have any noticeable voltage drop across them. If there is a voltage drop, then you have a bad driver or drivers which are holding the voltage down. The solution to this problem is to cut Pin 11 on the drivers, until the voltage drop across the current resistor is approximately $\varnothing$ volts. Usually the first and fourth drivers of the group which pertain to that particular current resistor should be cut first. These measurements should be made with a DVM under static conditions.


DATE:
FROM:
SUBJECT:

19 APRIL 1974
R. JENSEN

MISSING CAPACITOR

NUMBER: Sl005
CATEGORY: MEMORIES
MODEL: $8300,8301,8302$, AND 8117

On the 120016 K Memory and all Nova II Memories ( $4 \mathrm{~K}, 8 \mathrm{~K}$, and 16 K ), a de-coupling capacitor for -5 volts has inadvertantly never been installed. It is strongly recommended that this capacitor be installed when encountering any of these memories.

Symptoms:
When running a Memory Test, turning the Teletype off/on causes a Memory failure. Secondary method is a visual inspection. Holding board by connector, look for a 6.8 MF 35 V cap between -5 volt etch and ground. To the right of the sense amp for bit 14 and 15 is a crosshatch of etches. This is the ground connection for the cap. One of the etches between this crosshatch and the sense amp is -5 volts. This varies with each memory and also with Rev. level so more precise instructions cannot be given. (See ECO 2507 for precise location.)

Solution:
Install 6.8 ufd 35 tant capacitor (DG Part No. 103-000-002) to right of sense amp for bits 14 and 15 between -5 volts etch and ground etch. Use existing feedthru holes. ECO 2507 is generated to correct this problem.

Thanks to Parker Sutherland of the New England Depot for finding this problem and bringing this to attention.

|  | $\frac{y x \times 0}{234}$ | yox\| | $\begin{aligned} & x \times 0)_{2} \\ & (1(\sigma)!1) \end{aligned}$ | Ox | $\frac{\text { DRIVE LINE }}{\text { X0, }}$ |  | $\frac{16 \mathrm{~K}}{046}$ | $\frac{16 k}{049}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ADORL. | 111 |  | 111 | 11 | $\begin{aligned} & x, 0,0 \alpha \\ & \times 0 i, 03 \end{aligned}$ | $=$ | 447 | $=450$ |
| DRIVE, |  |  |  |  | $\times 04,06$ $\times 05,07$ |  | -52 | $=055$ |
|  | 111 | 11 | 111 | 11 |  |  |  | $=146$ |
|  |  |  |  |  | x 020,030 |  | 042 | $=445$ |
|  | 111 | 11 | 111 | 11 | $\times 040,050$ $\times 000$ |  | 141 <br> 044 | $=$44 <br> $=$ <br> 43 |
|  |  |  |  |  | X100, 110 |  | 634 033 | $=\begin{aligned} & 437 \\ & =436\end{aligned}{ }^{\text {c }}$ ( |
|  | 111 | 11 | 111 | 11 | $\times 120,130$ $\times 140,150$ $\times 100$ |  | 433 032 |  |
|  |  |  |  |  | $\times 160,170$ |  | 429 | $=432$ |
|  | 111 | 11 | 111 | 11 |  |  |  |  |
|  | 111 |  |  |  | Yo1,05 |  | 469 | $=472$ |
|  | 111 | 1 | 11 | 11 | y 02,06 $y 03,07$ |  | 066 | $=070$ $=069$ |
|  | 111 | 11 | 111 | 11 | y 000,020 |  | 065 | $=468$ |
|  |  |  |  |  | Y010,030 |  | 064 | = 467 |
| L | 111 | 11 | 111 | 11 | y 9050,070 |  | 062 | $=065$ |
|  |  |  |  |  | Y 100,120 |  | 060 | $=063$ |
|  | 111 | 11 | 111 | 11 | Y140, 160 |  | -58 | = U62 |
|  |  |  |  |  | y150,170 |  | ¢57 |  |
|  | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| L | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| L | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| L | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| $L$ | 111 | 11 | 111 | 11 |  |  |  |  |
|  | 111 | 11 | 11 | 1 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
| $L$ | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |
|  | 111 | 11 | 111 | 11 |  |  |  |  |
|  |  |  |  |  |  |  |  |  |

## FIELD ENGINEERING SUPPORT SOUTHBORO

## Technical Informarion Bullevina

DATE:
FROM: SUBJECT:

FEBRUARY 19, 1975
D. REED

VOLTAGE REGULATOR \& THRESHHOLD POTENTIOMETER ADJUSTMENTS FOR 8117 MEMORIES WITH ECO 2565 INSTALLED

| NUMBER: | S1.008 |
| :--- | :--- |
| CATEGORY: | MEMORIES |
| MODEL: | 8117 |

CATEGORY: MEMORIES MODEL:

A11 8117 16K Memories, Artwork 107-000-185-03 and above, with ECO 2565 installed should have R5 \& R198 adjusted as follows:

Voltage Regulator Pot (R5) fully clockwise Threshhold Pot (R198) fully counterclockwise

Rev 02 and below, or REV 03 and higher without ECO 2565, should not be field adjusted due to the fact that an adjustable voltage supply is required for +VINH.

The best means of ascertaining if ECO 2565 has been implemented is as follows:
(1) R73 510 OHM $\frac{1}{2}$ resistor deleted.
(2) C19, C36, C42, C52, C57, C68, C71, C84 l00pf capacitors changed to 220pf.
(3) R43, R80, Rlll, Rll4, Rl51, Rl55, R190, R199 3300 OHM resistors deleted.

See TIB Sl007 - Category: MEMORIES

## FIELD ENGINEERING SUPPORT SOUTHBORO

Technical Information Bulletin


DATE: 29 OCTOBER 1974
FROM: D. REED
SUBJECT: FIELD INSTALLATION OF ECO 2565

NUMBER: S1007
CATEGORY: MEMORIES MODEL: 8117

ECO 2565 balances the Read/Write currents of the 811716 K memories to allow them to pass Multiprogramming test. This ECO is implemented by changing the values of ten components for Rev . 3 \& 4 and eighteen components for Rev. 5 and above. Eight of these components are capacitors and resistors which are varied at test level to achieve optimum memory margins. Since the selection of these components requires special test equipment, it is not recommended that this change be attempted in the field. 8117 memories which require this change may be returned to a repair depot for updating per Data General Corporation standard repair procedures.

## FIELD ENGINEERING SUPPORT SOUTHBORO

## Technical Information Bulletion

DATE:
19 APRIL 1974
FROM:
R. JENSEN

SUBJECT: MISSING CAPACITOR

NUMBER: Sl005
CATEGORY: MEMORIES
MODEL: 8300, 8301, 8302
AND 8117

On the 1200 l6K Memory and all Nova II Memories $(4 \mathrm{~K}, 8 \mathrm{~K}$, and 16 K ), a de-coupling capacitor for -5 volts has inadvertantly never been installed. It is strongly recommended that this capacitor be installed when encountering any of these memories.

Symptoms:
When running a Memory Test, turning the Teletype off/on causes a Memory failure. Secondary method is a visual inspection. Holding board by connector, look for a 6.8 MF 35V cap between -5 volt etch and ground. To the right of the sense amp for bit 14 and 15 is a crosshatch of etches. This is the ground connection for the cap. One of the etches between this crosshatch and the sense amp is -5 volts. This varies with each memory and also with Rev. level so more precise instructions cannot be given. (See ECO 2507 for precise location.)

Solution:
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