

Documentation Package for DH201R

1. Schematics for SCZ-2.
2. SCZ-2 Manual
 - Programmers Reference
 - Users Manual
 - Installation
 - Troubleshooting
3. Disk Chassis Users Guide.
4. PPL (Provisioning Parts List).
5. Bootstrap patch (if applicable).
6. DS921 external cable drawing.
7. Internal chassis harness drawing (modified 005-19056).
8. Disk Cannister User's Guide.
9. Diagnostic/Utility tape for SCZ-2.
10. Quick Installation User's Guide for 80MB Conners.

Initialization Procedure

1. Cable DH201R up to J10 on MSE/14 processor.
2. Insert cannister into either slot of DH201R
3. Power units up.
4. Run low level formatter supplied on on SCZ-2 tape.
5. Run DFMTR to initialize DZ device (no need for surface analysis).
6. Unpack/n with patched tape.
7. Boot unit / ready to go.

DH201R as Configured

- 0 - This unit is supplied with a Connor 80MB single ended drive, mounted within the cannister.
- 0 - The terminator is on the Connor 80MB drive.
- 0 - The SCSI ID 0 is set on the 80MB drive.
- 0 - The controller is emulating a 50MB Zebra 6067 drive.
- 0 - The device code is set on the controller card. See SCZ-2 manual for details.
- 0 - The Reli diag and low level formatter is supplied on a seperate tape. Only the ROLM MHDRZ diagnostic @ 50MB will run from IDMS.
- 0 - If you low level format, then DFMTR must be run! Before you unpack, diskinit alone will not work!
- 0 - The low level formatter will remap any bad blocks, so that surface analysis is unecessary.

Bootstrap Modification for DH201R

The following is the required bootstrap modification to get the DH201R to run as a DA series disk on the Georgia Power configuration.

During the bootstrap when utilizing the SCZ-2 controller, the routine must wait until the full control bit is set. If this does not happen, then the disk will not boot. Installing the following patch to the bootstrap gives the disk controller enough time to set the full control bit. This patch should only be necessary when using the DA disk bootstrap, using the Zebra style bootstrap should eliminate this patch as the SCZ-2 is a Zebra emulating controller. This routine needs to be entered only ONCE during the boot procedure. If the wait state is done on every disk access, it takes an enormous amount of time to boot. It is only necessary to wait on the first disk access, after that the bootstrap and disk controller work fine together.

Address	Was	New	Mnemonic
-----	-----	-----	-----
000212	030007	000513	Jmp .+113 !jump to wait state
000325	000000	040433	STA 0 .+33 !save ACO original value
000326	000000	020434	LDA 0 .+34 !see if 1st time in routine, skip if not first time
000327	000000	101404	INC 0,0 SZR
000330	000000	000404	JMP .+4 !exit immediately, not 1st time in routine
000331	000000	040431	LDA 0 .+30 !load wait count
000332	000000	101404	INC 0,0 SZR !wait state
000333	000000	000777	JMP .-1 !wait until ACO = 0
000334	000000	020424	LDA 0 .+24 !ACO original value
000335	000000	030007	LDA 2,7
000336	000000	000655	JMP .-126 !return to bootstrap routine
000360	000000	000000	Temp storage of ACO's original value
000361	000000	000000	Wait count, will increment 177777 times before exiting, increasing this value decreases the wait time
000362	000000	177777	1st time thru check, once incremented the value becomes 0 and the routine is exited immediately not doing the wait state.



Notes for Using 5600 or 5300 on RDOS:

1. Leave the terminator packs in on the first and last controllers on the SCSI bus, ie, on a 2 controller installation, leave them in on both controllers.
2. Switch at location 6D, position 6:
This applies only to multi-controller installations.

 OFF: Bus lock cleared by IORST or by returning tape to BOT.
 ON: Bus lock cleared by IORST or by unloading tape.
3. Switch at location 20F:
 for RDOS only: Pos.1 OFF
 2 OFF
 3 ON
4. Prom at location 9G:
 4mm RDOS use "REV 9.1R"
 4mm AOS and AOS/VS: use "REV 9.1"

 8mm RDOS use "REV 9.0R"
 8mm AOS and AOS/VS use "REV 9.0"
5. Jumper E1-E2-E3
 RDOS: E1-E2 cut, E2-E3 installed.
 AOS and AOS/VS: E1-E2 installed, E2-E3 not installed.
6. Patch files are not applicable to RDOS, therefore the "Installation Macro" tape is not required for your system. It is only used on AOS/VS. For 16 bit AOS, contact factory for patch information.

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SEC-2

SPARE GATES

- LS00 K14 11,12,13
- LS00 L13 11,12,13
- LS02 H4 11,12,13
- LS08 A1 11,12,13
- LS10 D2 8,9,10,11
- LS10 G9 8,9,10,11
- LS11 FS 8,9,10,11,12,13,1,2
- 14 G6 12,13
- LS32 D7 11,12,13
- LS32 K10 11,12,13
- LS32 F2 11,12,13
- 26S02 C1 9,10,11,12,13,14,15

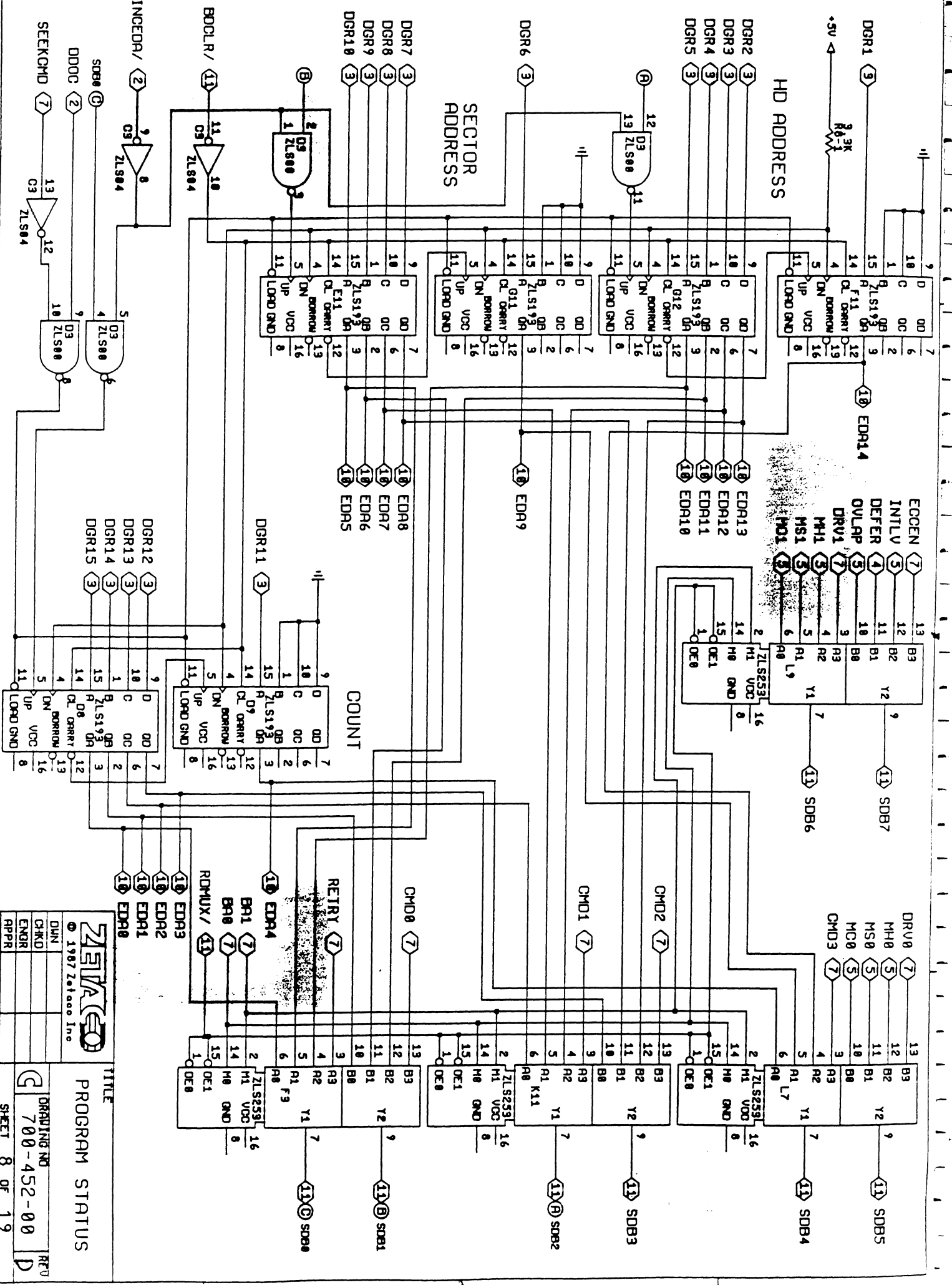
REVISION HISTORY

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B	1023	SHTS 4,16	CJK	12-07		
C	1041	SHT 17,18	CJK	1-88		
D	1081	SHT 7,10	CJK	3-88		

507-2 507-2F
507-2F

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OWN	
CHKD	
ENGR	
APPR	



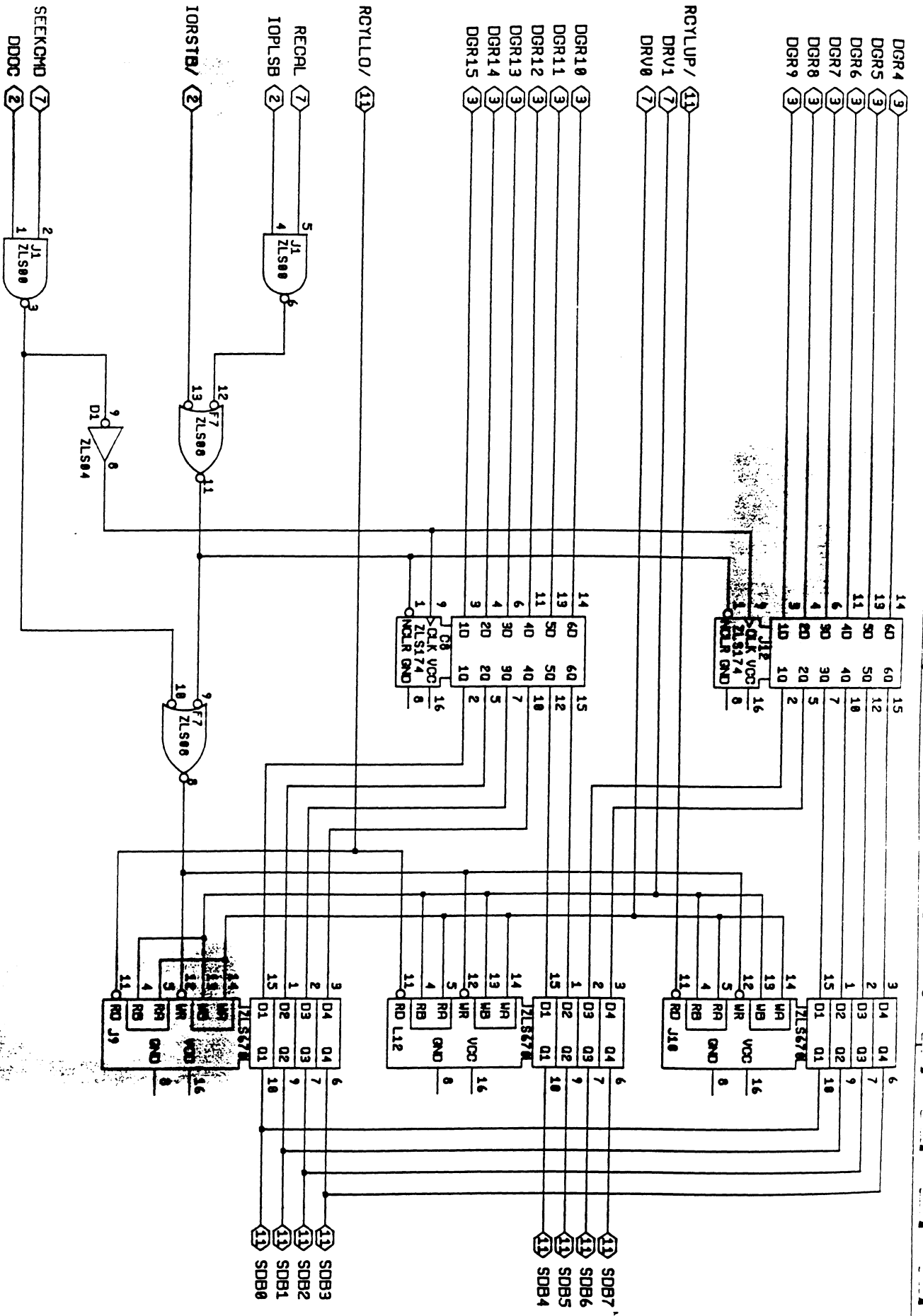
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PROGRAM STATUS

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APPR		

DRAWING NO
700-452-00

SHEET 8 OF 19

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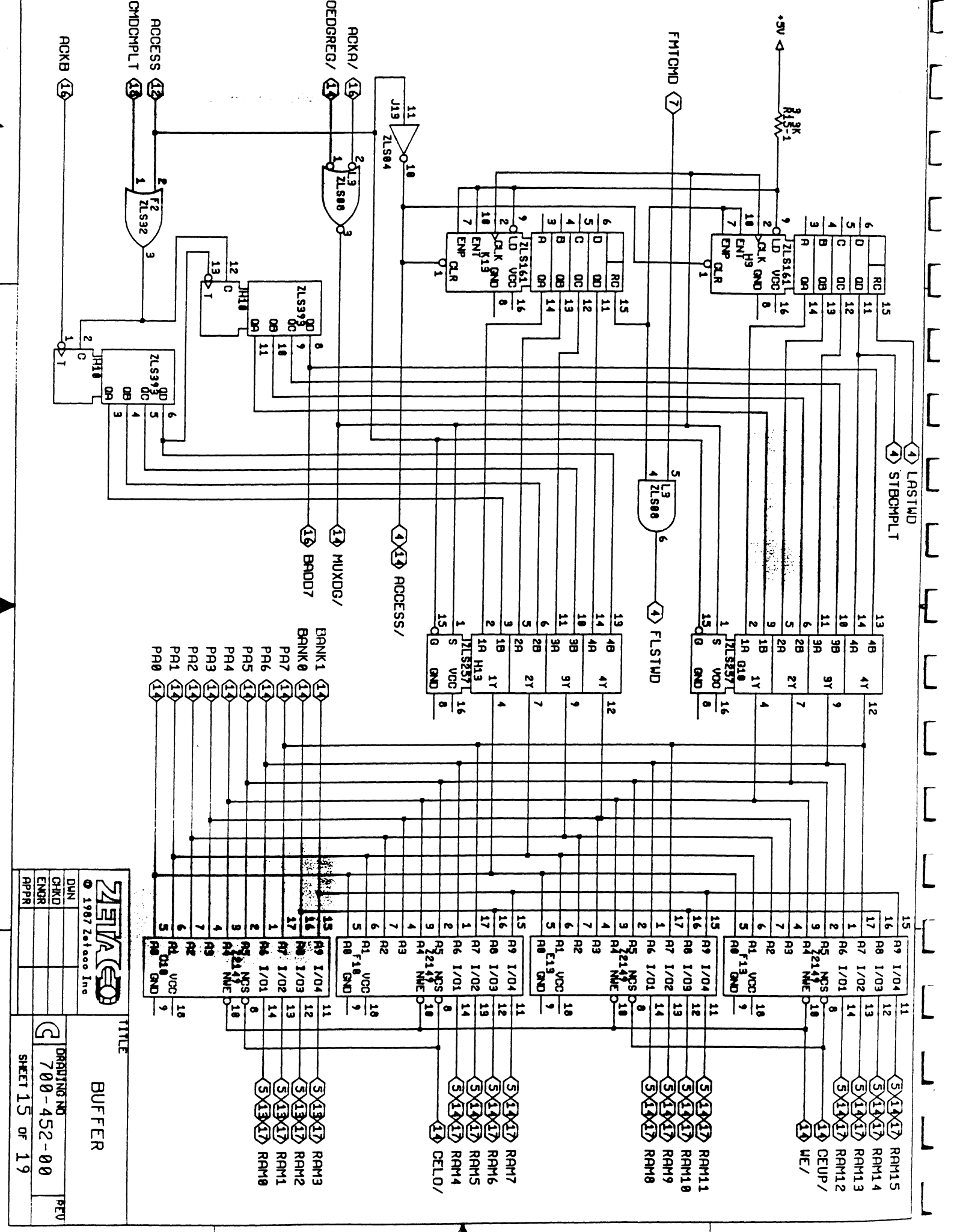
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9

2

1



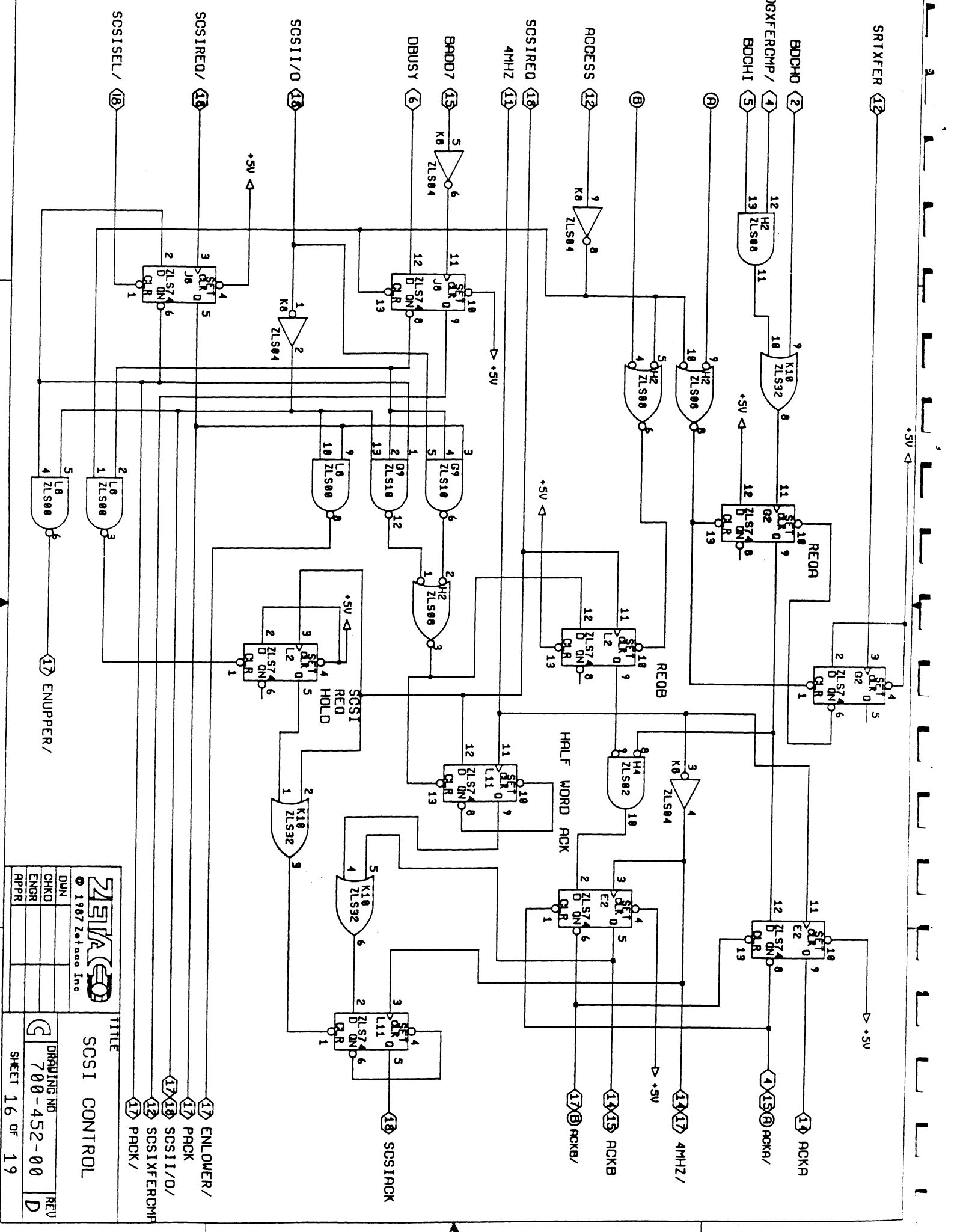
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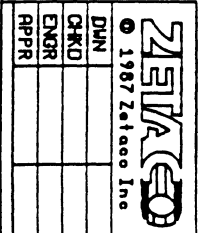
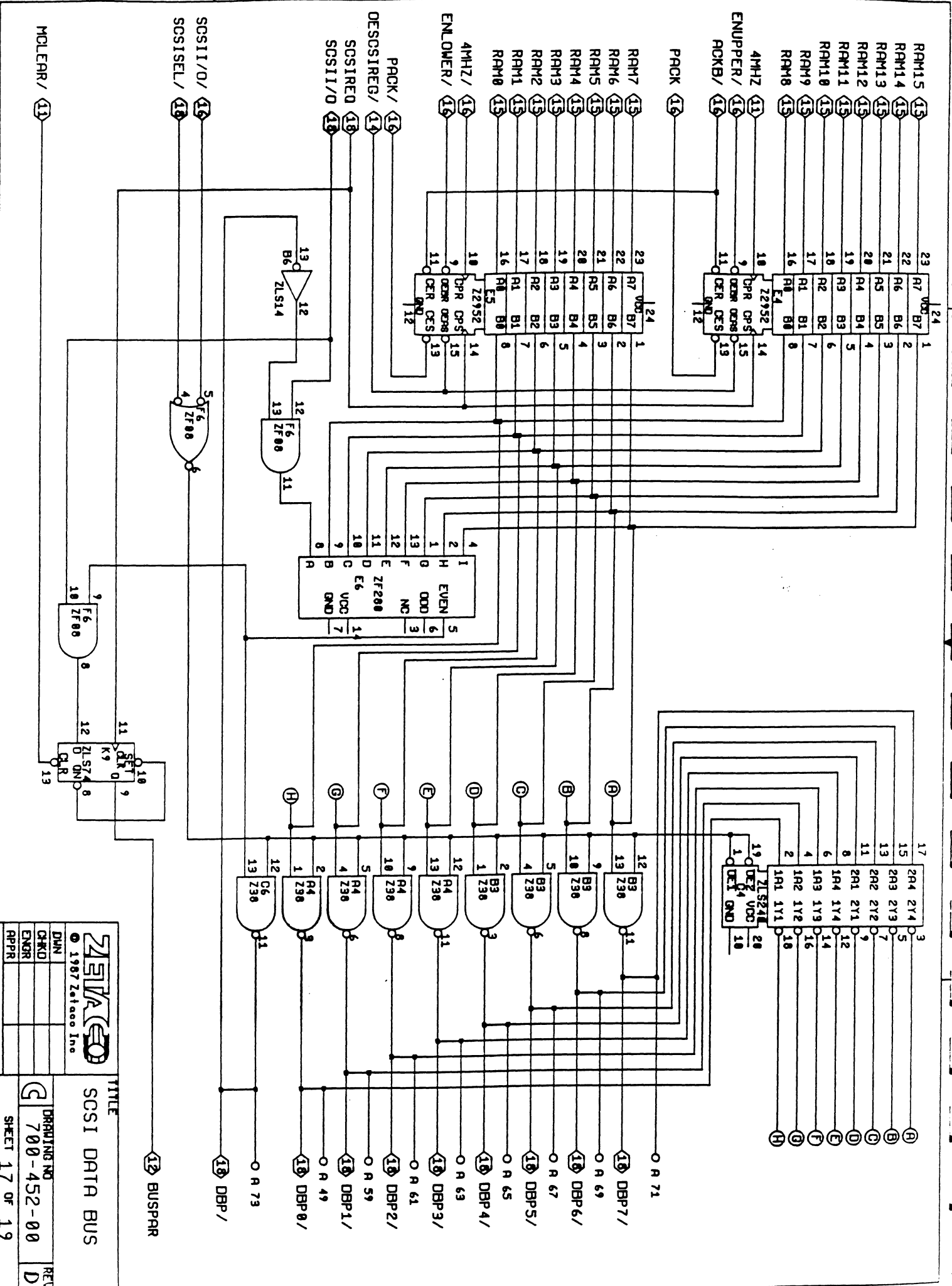
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SHEET 15 OF 19

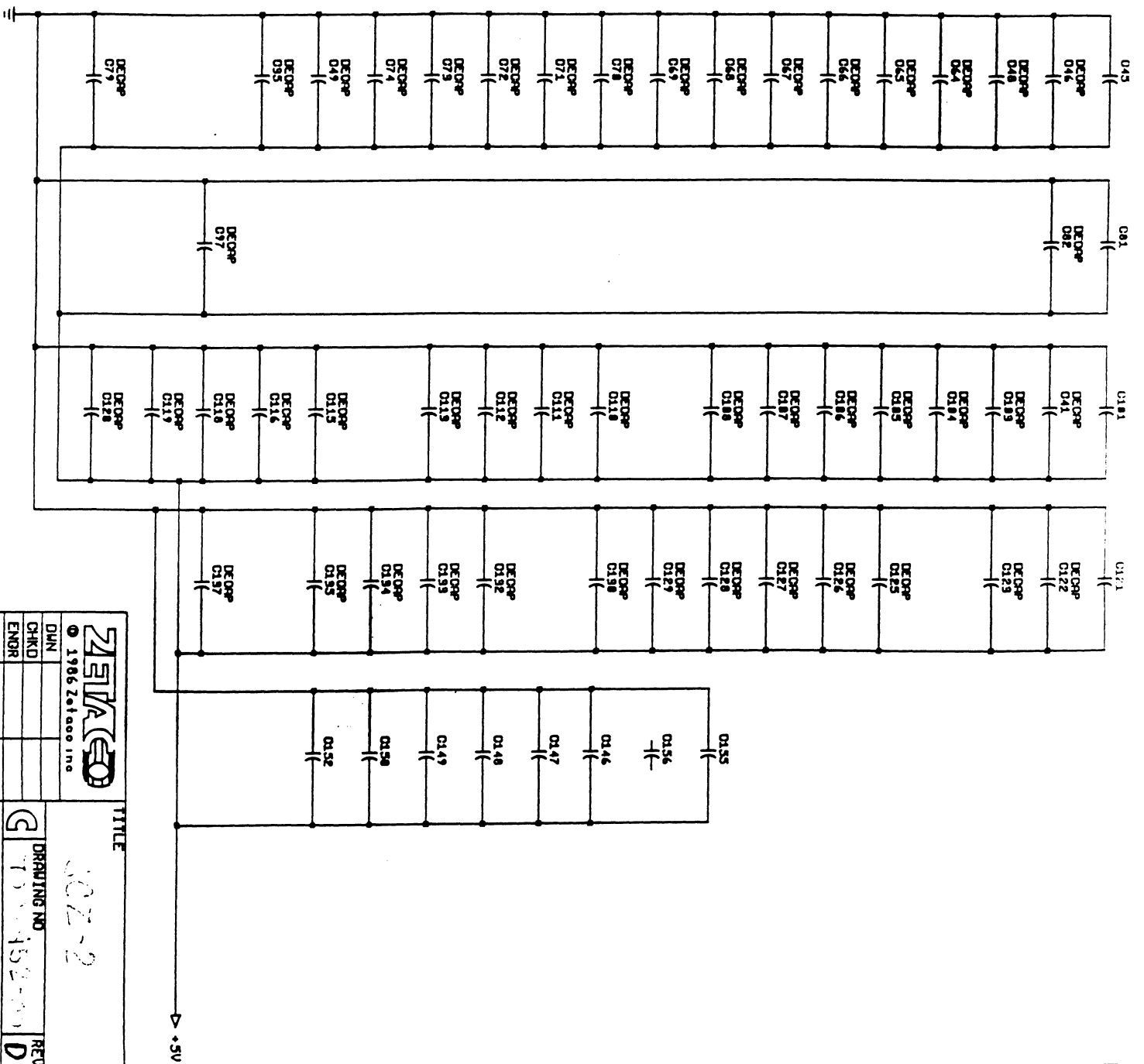
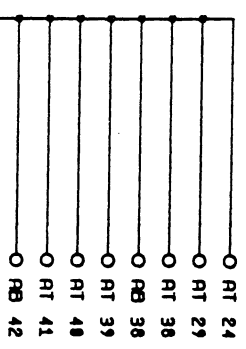
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1987 Zetec Inc.		TITLE SCSI CONTROL	
DWN	CHKD	DRAWING NO.	700-452-00
ENGR		REV	D
APPR		SHEET	16 OF 19



TITLE	
SCSI DATA BUS	
DWN	DRAWING NO
CHKD	700-452-00
ENR	
APPR	
	REV
	D
SHEET 17 OF 19	



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TITLE

302-2

DRAWING NO

70-452-001

REV

G

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D

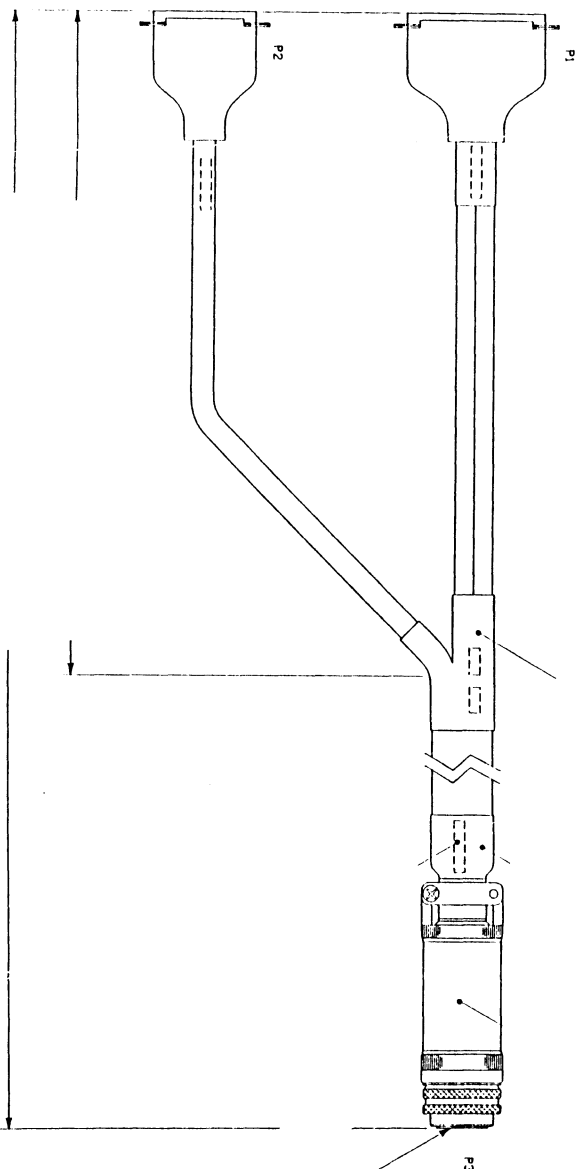
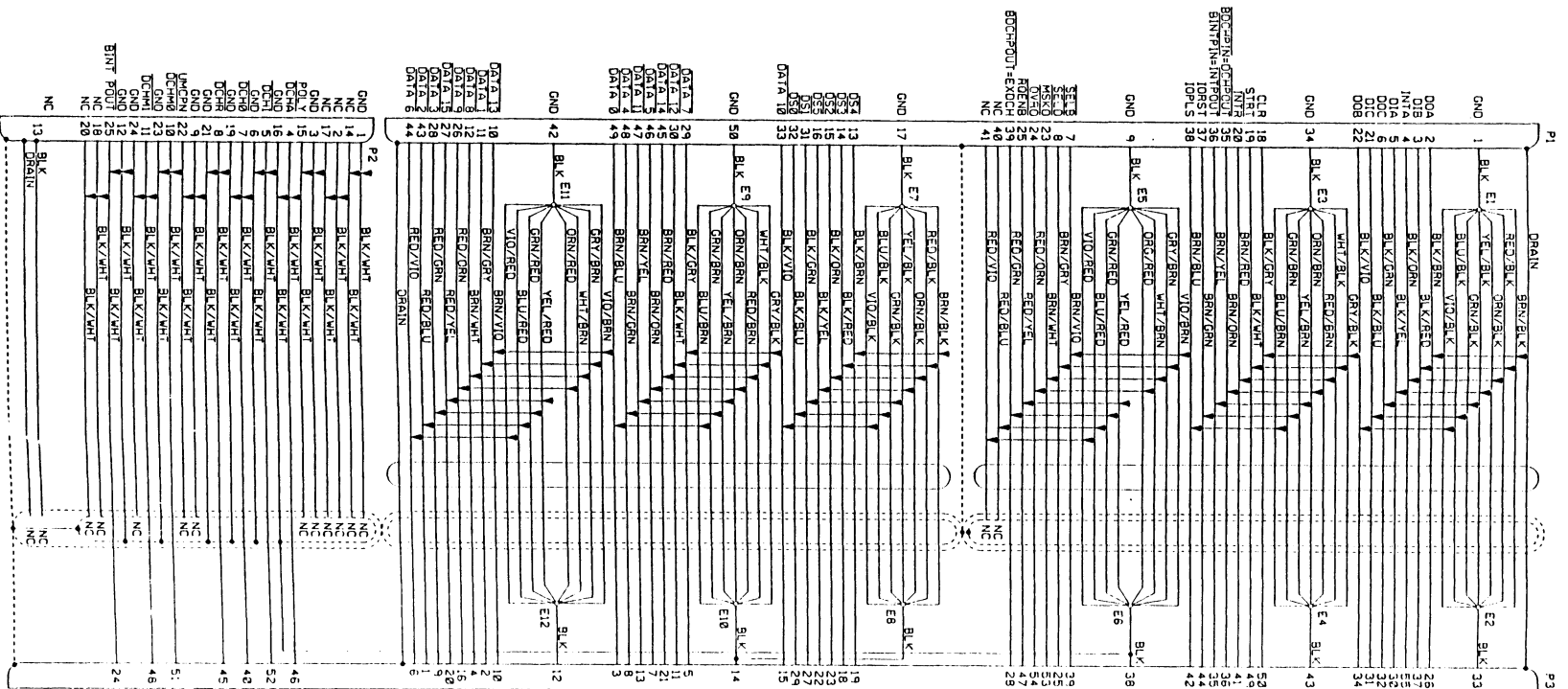
DWN

CHKD

ENGR

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A B C D E F G H



I/O Interface Cable
 Assembly - RoIm
 Computer to DH201R
 Disk Chassis