

Integration Guide

SKM Subsystems

SKM Subsystems have been tested and verified by Engineering for use with the following Data General CPUs and operating systems.

SECTION A: DATA GENERAL PROCESSORS

Processors

MV/7800 C, U
MV/7800 XP
MV/8000 II, C
MV/10000
MV/15000
MV/20000
◆ MV/40000
MV/9500

Operating Systems

◆◆ AOS/VS (Rev. 5.0 to 7.67)
AOS/VS II (Rev. 1.12 to 2.00)
DG/UX (Rev. 4.02)

NOTES

- ◆ Requires MV/40000 paddleboard assembly, which has been specially designed to fit within the MV/40000 FCC bulkhead.
- ◆◆ SKM 1244, 1344 and 1304 Tape Subsystems require AOS/VS Rev. 7.65.

If you know of CPUs other than those listed in Section A that are running with SKM Subsystems, please write, telex or fax our Customer Support Team with the information so the CPUs can be verified and possibly added to this integration guide.



SECTION B: VERIFIED DISK & TAPE SUBSYSTEMS

Tape

SKM-1244 Subsystem
SKM-1344 Add-on Subsystem
SKM-1304 Add-on Drive

Drive

Exabyte, 8mm
Exabyte, 8mm
Exabyte, 8mm

Disk

SKM-1231 Subsystem
SKM-1232 Subsystem
SKM-1233 Subsystem
SKM-1331 Add-on Subsystem
SKM-1332 Add-on Subsystem
SKM-1333 Add-on Subsystem
SKM-1301 Add-on Drive
SKM-1302 Add-on Drive
SKM-1303 Add-on Drive

Drive

330 MB, Wren Runner
601 MB, Wren V
1.2 GB, Wren VII
330 MB, Wren Runner
601 MB, Wren V
1.2 GB, Wren VII
330 MB, Wren Runner
601 MB, Wren V
1.2 GB, Wren VII

NOTE: All of the above subsystems are SCSI differential based products.

VOLTAGE SPECIFICATION

SKM-1901 Chassis: 90-132 VAC
180-264 VAC
47-63 Hz, 5 amps

SKM SUBSYSTEM FEATURES SUMMARY

- 19" Relay Rack-mountable
- Support for up to four drives per chassis (maximum of 2 tapes per chassis)
- Tape Controller supports Data General DPJ 6236 and 6239 drivers (maximum of 4)
- Disk Controller supports Data General DPJ 6236 and 6239 drivers (maximum of 7)
- Controllers require an I/O ONLY slot in Data General chassis
- Controller software supports multiple IOC configurations
- Disk Mirroring
- Back-up 2.1 GB of data on one 8mm tape cartridge
- Supports both asynchronous and synchronous SCSI bus data transfers