

technical memo

HARDWARE INFORMATION, CUSTOMER SUPPORT

HICS.8

TO: MARK 9 Users
FROM: Tech Support
DATE: June 29, 1984
SUBJ: MARK 9 SYSTEMS

SYSTEM POWER SUPPLY

Revision C (or earlier) MARK 8 power supplies (Assy 62003) do not supply enough +5V BBU current for the MARK 9 CPU. A factory modification is required. Revision D and later power supplies have been modified. For ease of identification, the assembly ID tag has been moved to the front of the P/S on Rev D (and later) power supplies. This change will also be applied to upgraded power supplies.

The MARK 5 P/S (Assy 64003) is not upgradable.

Warranty P/S - Return to POINT 4 for a NO CHARGE upgrade.

Non-Warranty P/S - The standard repair price will be charged.

NOTE: This modification is not field installable.

LOTUS CACHE MEMORY FOR MARK 9

Field Change Notice #011 will be required on all sizes of LCM's with serial numbers below 1456. All LCM's above this S/N are already modified (see FCN #011).

GENERAL NOTES:

1. These requirements are not necessary for MARK 5/8 CPUs but will not interfere with their performance.
2. The MARK 9 CPU is pluggable into any MARK 5/8 chassis Assy 65003 CPU slot without any modifications.
3. The Universal MARK 9 CPU uses the same general rules as the MARK 5/8 CPU Universal.
4. IRIS users must use Revision 8.2C or later.