

PROGRAM

Address Test

TAPES

Binary: 095-000002

ABSTRACT

Address Test is a maintenance routine designed to test the memory address selection logic.

MEMORY ADDRESS TEST

31.	ABSTRACT				
1	MEMORY ADDRESS TEST IS A MAINTENANCE				
3	PROGRAM DESIGNED TO DETECT MALFUNCT-				
3	IONS IN THE MEMORY ADDRESS SELECTION				
3	LOGIC. THE PROGRAM FILLS MEMORY WITH				
3	A ADDRESS PATTERN(C(ADDRESS)=ADDRESS)				
3	SUCESSFUL READ BACK OF THE PATTERN IS				
3	PROOF THAT ALL LOCATIONS EXIST.				
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12.	MACHINE REQUIREMENTS				
32.1	STANDRED NOVA PROCESSOR				
12.2	4K READ/WRITE MEMORY. SEE SECTION 7.				
33 •	SWITCH SETTINGS				
3	STARTING ADDRESS = 0				
3	SWITCHS 1-15 = SYNC ADDRESS				
	OPERATING PROCEEDURE				
34-1	READ IN THE PROGRAM VIA THE BINARY LOADE				
14.2	SET THE SWITCHES TO 89889				
34.3	PRESS START				
34-4	THE PROGRAM WILL RUN UNTILL A ERROR				
3	IS DETECTED OR IT IS MANUALLY STOPPED.				
35 •	PROGRAM OUTPUT/ERROR DISCRIPTION				
35-1	WHEN A ERROR IS DETECTED THE PROGRAM				
	WILL HALT. EXAMINE ACS TO OBTAIN THE				
3	ADDRESS OF THE FAILURE. EXAMINE ACE				
3	TO OBTAIN THE DATA READ FROM THE ADD-				
3	RESS SPECIFIED BY AC3.				
15.2	IF THE CARRY FLAG IS SET WHEN THE HALT				
3	IS EXECUTED THE MEMORY FAILED UPON				
3	READING OUT THE LOCATION JUST AFTER				
3	HAVING STORED IT. THIS SUGGEST FAULTY				
3	CURRENTS AT THE FAILING LOCATION. IF				
3	THE CARRY FLAG IS RESET WHEN THE HALT				
3	IS EXECUTED THE LOCATION FAILED AFTER				
3	HAVING BEEN SUCESSFULLY READ ONCE.				
3	THIS SUGGEST THAT OTHER ADDRESS MAY				
3	AFFECT THE FAILING ADDRESS.				
35.3	RECORD THE ADDRESS AND VALUE AT EACH				
3	HALT. PRESS CONTINUE. TRY TO FORM A				
3	PATTERN OF ERRORS. ARE ALL THE ERRORS				
3	AT ONE X OR Y LINE VALUE? DOES THE FAIL-				
3	ING LOCATION CONTAIN THE DATA OF ANOTHER				
3	ADDRESS?				
35+4	IF THE OPERATOR WISHES TO SCOPE THE				
3	FAILURE HE MAY SYNC THE SCOPE AT A74				
3	(A "P" PULSE). THE PROGRAM WILL ISSUE				
3	THIS PULSE WHENEVER THE CONSOLE SWITCHS				
3	ARE EQUAL TO THE PATTERN ADDRESS. THE				
3	THIRD MEMORY CYCLE AFTER SYNC THE MEMORY				
3	WILL BE REFFERANCED WITH A LOAD OF STORE				
3	CYCLE AT THE ADDRESS SPECIFIED .				

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15.5		IT IS OFTEN USEFUL TO OBSERVE SELECTION
3		CURRENTS AT ADDRESS OTHER THAN THE ADDRESS
3		SYNC ON. SYNCING ON ADDRESS 400(Y-04)
3		THERE SHOULD BE NO CURRENT ON Y-14
3		THE USER IS CAUTIONED TO SET A PROPER
3		TIME REFFERANCE ON THE SCOPE SUCH THAT
3		CURRENTS PRODUCED BY EXECUTION OF THE
3		PROGRAM INSTRUCTIONS DO NOT CONFUSE THE
1	•	OBSERVATION.
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36.	PROGRAM	DISCRIPTION
3		THE PROGRAM CONSIST OF A RELATIVELY
3		SIMPLE STORE AND CHECK MEMORY ROUTINE.
1		ON THE FIRST PASS AFTER STARTING, THE
		ADDRESS OF THE PATTERN LOCATION IS STORB
3		IN THE PATTERN LOCATION. THE LOCATION
		IS THEN READ OUT AND CHECKED FOR ERRORS.
3		THE PROCESS CONTINUES WITH SUCESSIVE
3		LOCATIONS UNTILL THE END OF THE PATTERN
3		IS REACHED. THE CARRY WILL BE COMPLEMENT
3		AT THE END OF A PASS. THE PROGRAM AGAIN
1		SCANS MEMORY READING AND CHECKING . BUT
1		NOT STORING. THE DATA.
37.	LIMITATI	ONS/NISC
3		THE SIZE OF THE MEMORY TO BE TESTED MAY
į		BE MODIFIED BY CHANGING THE VALUE OF
		PROGRAM LOCATION 17.
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88888 125	5040 A:	MOVO 1.1.	START HERE, SET CARRY FLAG
00001 034	4017	LDA 3.IADR	•
00002 020	9016	LDA Ø.WC	#WORD COUNT+
00003 064	1477 B:	READS 1	FREAD THE SWITCHES.
99994 166	6015	ADC# 3,1,SNR	JIF SAME AS ADDRESS
00005 060	377	NIOP CPU	SEND SYNC PULSE (A74).
00006 175	5402	INC 3.3.52C	JINCREMENT PATTERN ADDRESS-
00007 055	5400	STA 3.0.3	STORE ADDRESS DATA
09316 931	400	LDA 2.0.3	FREAD BACK THE DATA
00011 156	5414	SUB# 2,3,52R	CHECK THE DATA
09912 063	3677	HALT	C(2) = ERROR WORD C(3) = ADDRESS
09913 101	404	INC 0,0,SZR	COUNT WORDS, COMP CARRY ON END
88814 688	9003	JMP B	
00015 600	1001	JMP A+1	JEND OF A PASS
00016 178	1226 WC:	-7600+20	SIZE OF THE PATTERN.
00017 000	0017 IADR:	•	FIRST PATTERN LOCATION-1.
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